

| Ref # | Hits | Search Query                      | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|-----------------------------------|---|------------------|---------|------------------|
| L1    | 9101 | embedded near6 processor          | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:21 |
| L2    | 1354 | "built in self test"              | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:26 |
| L3    | 227  | "pseudo-random pattern generator" | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:23 |
| L4    | 1908 | lfsr                              | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:24 |
| L5    | 2050 | I3 or I4                          | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:24 |
| L6    | 0    | I1 with I5                        | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:24 |
| L7    | 5    | I1 same I5                        | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:25 |
| L8    | 147  | I1 and I5                         | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:24 |
| L9    | 143  | I2 same processor                 | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:26 |
| L10   | 209  | I2 same controller                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:26 |

|     |     |             |   |    |    |                  |
|-----|-----|-------------|---|----|----|------------------|
| L11 | 259 | I9 or I10   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/05/18 09:31 |
| L12 | 8   | I11 same I5 | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/05/18 09:27 |

| Ref # | Hits  | Search Query                      | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|-------|-----------------------------------|---|------------------|---------|------------------|
| L1    | 129   | "scan path testing"               | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:00 |
| L2    | 3509  | "embedded processor"              | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:00 |
| L3    | 71517 | "non volatile memory"             | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:01 |
| L4    | 227   | "pseudo-random pattern generator" | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:04 |
| L5    | 0     | I1 with I2                        | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:02 |
| L6    | 0     | I1 same I2                        | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:02 |
| L7    | 1     | I1 and I2                         | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:03 |
| L8    | 0     | I2 same I4                        | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:03 |
| L9    | 0     | I2 and I4                         | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/05/18 09:03 |

| Ref # | Hits  | Search Query           | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|-------|------------------------|-------|------------------|---------|------------------|
| L1    | 89    | (714/728).CCLS.        | USPAT | OR               | OFF     | 2005/05/18 13:15 |
| L2    | 1177  | "embedded processor"   | USPAT | OR               | OFF     | 2005/05/18 13:15 |
| L3    | 249   | "on-chip processor"    | USPAT | OR               | OFF     | 2005/05/18 13:15 |
| L4    | 1423  | I2 or I3               | USPAT | OR               | OFF     | 2005/05/18 13:15 |
| L5    | 0     | I4 and I1              | USPAT | OR               | OFF     | 2005/05/18 13:16 |
| L6    | 24660 | "714".clas.            | USPAT | OR               | OFF     | 2005/05/18 13:16 |
| L7    | 152   | I6 and I4              | USPAT | OR               | OFF     | 2005/05/18 13:18 |
| L8    | 7     | I7 and (pseudo-random) | USPAT | OR               | OFF     | 2005/05/18 13:19 |